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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Y. TAKAHASHI, et al.
Application No.: 10/722,461
Filed: November 28, 2003
For: SEMICONDUCTOR MEMORY DEVICE AND
SEMICONDUCTOR INTEGRATED CIRCUIT
Art Unit: 2827
Examiner: S. MAI

RESPONSE TO ELECTION/RESTRICTION REQUIREMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

July 28, 2005

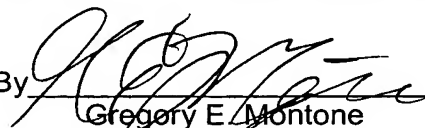
Sir:

In response to the Restriction Requirement dated April 28, 2005, the period of response for which extension of time is requested in the attached Petition for Extension of Time, Applicants hereby elect **Species II**, without traverse, and which claims 1-3 and 7 are readable on the elected species.

To the extent necessary, Applicants petition for an extension of time under 37 CFR 1.136. Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to the Deposit Account No. 01-2135 (Docket No. 1374.43253X00), and please credit any excess fees to such Deposit Account.

Respectfully submitted,
ANTONELLI, TERRY, STOUT & KRAUS, LLP

By


Gregory E. Montone
Reg. No. 28,141

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